#### Exercise Sheet 7

#### Exercise 1 (Memory Management)

- - $\boxtimes$  Buddy memory allocation
- With which memory management methods do external fragmentation occur?
   □ Static partitioning
   □ Dynamic partitioning
   □ Dynamic partitioning
  - $\boxtimes$  Buddy memory allocation
- 3. How can external fragmentation be fixed? By defragmentation. For virtual memory, external fragmentation is irrelevant.

4.	Which memory	management	method searches	for the block,	which fits best?
	$\Box$ First Fit	$\Box$ Next Fit	$\boxtimes$ Best fit	$\Box$ Random	

- 6. Which memory management concept fragments quickly the large area of free space at the end of the address space?
  □ First Fit □ Next Fit □ Best fit □ Random
- 7. Which memory management concept selects random a free and appropriate block?
  □ First Fit
  □ Next Fit
  □ Best fit
  □ Random
- 8. Which memory management concept searches for a free block, starting from the latest allocation?
  - $\Box \text{ First Fit } \Box \text{ Next Fit } \Box \text{ Best fit } \Box \text{ Random}$
- 9. Which memory management concept produces many mini-fragments and is slow?
  □ First Fit □ Next Fit □ Best fit □ Random

# Exercise 2 (Buddy Memory Allocation)

The Buddy method for allocating memory to processes shall be used for a memory with a capacity of 1024 kB. Perform the provided operations and give the occupancy state of the memory after each operation.

					1024	4 KB	
65 KB Anforderung => A	А	128	КВ	256	КВ	512	КВ
30 KB Anforderung => B	А	B 32	64 KB	256	КВ	512	КВ
90 KB Anforderung => C	A	B 32	64 KB	С	128 KB	512	КВ
34 KB Anforderung => D	А	B 32	D	С	128 KB	512	КВ
130 KB Anforderung => E	A	B 32	D	С	128 KB	E	256 KB
Freigabe C	А	B 32	D	128 KB	128 KB	E	256 KB
	А	B 32	D	256	КВ	E	256 KB
Freigabe B	А	32 32	D	256	КВ	Е	256 KB
5	A	64 KB	D	256	КВ	E	256 KB
275 KB Anforderung => F Nicht möglich, weil keine 275 kB am Stück frei	А	64 KB	D	256	КВ	E	256 KB
145 KB Anforderung => G	А	64 KB	D	C	5	E	256 KB
Freigabe D	A	64 KB	64 KB	(	3	E	256 KB
	А	128	КВ	(	9	E	256 KB
Freigabe A	128 KB	128	КВ	(	3	E	256 KB
	256	6 КВ		(	6	E	256 KB
Freigabe G	128 KB	128	KB	256	КВ	E	256 KB
			512	КВ		E	256 KB
Freigabe E			512	КВ		256 KB	256 KB
			512	КВ		512	КВ
					102	4 KB	

## Exercise 3 (Real Mode and Protected Mode)

- Describe the functioning of the real mode.
   Each process can access the entire memory, which can be addressed.
- 2. Why is it difficult to use real mode for multitasking operation mode? It provides no memory protection.

- 3. Describe the functioning of the protected mode. Each process can only access its own virtual memory. Virtual memory addresses translates the CPU with the MMU into physical memory addresses.
- 4. What is virtual memory?

Each process has a separate address space. This address space is an abstraction of the physical memory. It implements virtual memory. It consists of logical memory addresses, which are numbered from address 0 upwards and it is independent from the storage technology used and the existing expansion options.

- 5. Explain, why virtual memory helps to better utilize the main memory. Processes do not need to be located in one piece inside the main memory. Therefore, the external fragmentation of the main memory is not a problem.
- 6. What is mapping? The virtual memory is mapped to the physical memory.
- 7. What is swapping? The process of relocating data from the main memory to the SD-D/HDD and back.
- 8. Which component of the CPU is used to implement virtual memory? Memory Management Unit (MMU).
- Describe the function of the component from subtask 8.
   Virtual memory addresses are translated into physical memory addresses by the CPU using the MMU.
- 10. Name a virtual memory concept. **Paging.**
- 11. What sort of fragmentation does occur with the concept of subtask 10? Internal fragmentation. It can only occur in the last page of each process.
- 12. What causes a page fault exception to occur?A process tries to access a page, which is not located in the physical main memory.
- 13. What is the reaction of the operating system, when a page fault exception occurs?

The operating system handles the page fault exception by executing these steps:

• Allocate the page by using the controller and the device driver on the swap memory (SSD/HDD).

- Copy the page into a free page of the main memory.
- Update the page table.
- Return control to the process. The process next tries to execute again the instruction that caused the page fault.
- 14. What causes an access violation exception or general protection fault exception to occur?

A process tried to access a virtual memory address, which it is not allowed to access.

15. What is the consequence (effect) of an access violation exception or general protection fault exception?

In some legacy Windows operating systems, segmentation faults often caused system crashes and resulted in a blue screen. In Linux, the signal SIGSEGV is returned as a result.

- 16. What contains the kernelspace? The operating system kernel and kernel extensions (drivers).
- 17. What contains the userspace?

The currently running process, which is extended with swap memory (Windows: page file).

## Exercise 4 (Memory Management)

Please mark for each one of the following statements, whether the statement is true or false.

- 1. Real mode is suited for multitasking systems.  $\hfill \Box$  True  $\hfill \Box$  False
- 2. In protected mode, each process is executed in its own copy of the physical address space, which is protected from other processes.
  ☑ True □ False
- 3. When static partitioning is used, internal fragmentation occurs.  $\boxtimes$  True  $\square$  False
- 4. When dynamic partitioning is used, external fragmentation cannot occur.  $\Box$  True  $~\boxtimes$  False
- 5. With paging, all pages have the same length.  $\square$  True  $\square$  False
- 6. One advantage of long pages is little internal fragmentation.  $\hfill \hfill \hf$
- 7. A drawback of short pages is that the page table gets bigger.  $\boxtimes$  True  $\square$  False
- 8. When paging is used, the MMU translates the logical memory addresses into physical memory addresses.

  ⊠ True
  □ False
- 9. Modern operating systems (for x86) operate in protected mode and use only paging.

  ⊠ True
  □ False

## Exercise 5 (Page Replacement Strategies)

- 1. Why is it impossible to implement the optimal replacement strategy OPT? Because it is not possible to predict the future and therefore the future request sequence is unknown.
- 2. Perform the access sequence with the replacement strategies Optimal, LRU, LFU and FIFO once with a cache with a capacity of 4 pages and once with 5 pages. Also calculate the hit rate and the miss rate for all scenarios.

Optimal rep	olac	em	ent	st	rat	egy	• (0	)P	Г):															
Requests:	1	3	5	4	2	4	3	2	1	0	5	3	5	0	4	3	5	4	3	2	1	3	4	5
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Page 2:		3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3
Page 3:			5	5	2	2	2	2	2	2	5	5	5	5	5	5	5	5	5	5	5	5	5	5
Page 4:				4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
	Hit Mis	rat ssra	e: ate	:	-	15/ 9/	24 24	=	0.6 0.3	525 875														
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Replacement strategy Least Frequently Used (LFU):

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Page 2:		3	3	3	3	3	3	3	1	1	1	1	1	1	4	4	4	4	4	4	4	4	4	4
Page 3:			5	5	5	5	5	5	5	0	0	0	0	0	0	0	0	0	0	2	2	2	2	2
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Requests: Page 1:	1	<b>3</b>	<b>5</b>	<b>4</b>	<b>2</b>	<b>4</b>	<b>3</b>	<b>2</b>	1	<b>0</b>	<b>5</b> 0	<b>3</b>	<b>5</b>	<b>0</b>	<b>4</b> 0	<b>3</b> 0	<b>5</b> 0	<b>4</b> 0	<b>3</b> 0	<b>2</b> 0	<b>1</b> 0	<b>3</b>	<b>4</b> 0	<b>5</b> 0
Requests: Page 1: Page 2:	1	<b>3</b> 1 3	<b>5</b> 1 3	<b>4</b> 1 3	<b>2</b> 1 3	<b>4</b> 1 3	<b>3</b> 1 3	<b>2</b> 1 3	<b>1</b> 1 3	<b>0</b> 0 3	<b>5</b> 0 3	<b>3</b> 0 3	<b>5</b> 0 3	<b>0</b> 0 3	<b>4</b> 0 3	<b>3</b> 0 3	<b>5</b> 0 3	<b>4</b> 0 3	<b>3</b> 0 3	<b>2</b> 0 3	<b>1</b> 0	<b>3</b> 0 1	<b>4</b> 0 1	<b>5</b> 0 1
Requests: Page 1: Page 2: Page 3:	1	<b>3</b> 1 3	<b>5</b> 1 3 5	<b>4</b> 1 3 5	<b>2</b> 1 3 5	<b>4</b> 1 3 5	<b>3</b> 1 3 5	<b>2</b> 1 3 5	<b>1</b> 3 5	<b>0</b> 3 5	<b>5</b> 0 3 <b>5</b>	<b>3</b> 0 <b>3</b> 5	<b>5</b> 0 3 <b>5</b>	<b>0</b> 3 5	<b>4</b> 0 3 5	<b>3</b> 0 <b>3</b> 5	<b>5</b> 0 3 5	<b>4</b> 0 3 5	<b>3</b> 0 <b>3</b> 5	<b>2</b> 0 3 5	<b>1</b> 0 1 5	<b>3</b> 0 1 <b>3</b>	<b>4</b> 0 1 3	<b>5</b> 0 1 3
Requests: Page 1: Page 2: Page 3: Page 4:	1	3 1 3	5 1 3 5	<b>4</b> 1 3 5 4	<b>2</b> 1 3 5 4	<b>4</b> 1 3 5 <b>4</b>	<b>3</b> 1 3 5 4	<b>2</b> 1 3 5 4	<b>1</b> 3 5 4	<b>0</b> 3 5 4	5 0 3 5 4	<b>3</b> 0 <b>3</b> 5 4	<b>5</b> 0 3 <b>5</b> 4	<b>0</b> 3 5 4	<b>4</b> 0 3 5 4	<b>3</b> 0 <b>3</b> 5 4	<b>5</b> 0 3 <b>5</b> 4	<b>4</b> 0 3 5 4	<b>3</b> 0 <b>3</b> 5 4	<b>2</b> 0 3 5 4	1 0 1 5 4	<b>3</b> 0 1 3 4	<b>4</b> 0 1 3 4	<b>5</b> 0 1 3 5
Requests: Page 1: Page 2: Page 3: Page 4: Page 5:	1	3 1 3	5 3 5	<b>4</b> 1 3 5 4	2 1 3 5 4 2	<b>4</b> 3 5 <b>4</b> 2	<b>3</b> 1 5 4 2	2 1 3 5 4 2	<b>1</b> 3 5 4 2	0 3 5 4 2	5 3 5 4 2	<b>3</b> 0 <b>3</b> 5 4 2	5 3 5 4 2	<b>0</b> 3 5 4 2	<b>4</b> 0 3 5 <b>4</b> 2	<b>3</b> 0 3 5 4 2	5 0 3 5 4 2	<b>4</b> 0 3 5 <b>4</b> 2	<b>3</b> 0 3 5 4 2	2 0 3 5 4 2	1 0 1 5 4 2	<b>3</b> 0 1 <b>3</b> 4 2	<b>4</b> 0 1 3 <b>4</b> 2	5 0 1 3 5 2

- 3. What is the key message of Laszlo Belady's anomaly? FIFO produces worse results for certain access patterns with increased memory.
- 4. Show Belady's anomaly by performing the access sequence with the replacement strategy FIFO once with a cache with a capacity of 3 pages and once with 4 pages. Also calculate the hit rate and the miss rate for both scenarios.

Requests:	3	2	1	0	3	2	4	3	2	1	0	4
Page 1:	3	3	3	0	0	0	4	4	4	4	4	4
Page 2:		2	2	2	3	3	3	3	3	1	1	1
Page 3:			1	1	1	2	2	2	2	2	0	0
	Hit Mis	rat ssra	e: ate	:	3/1 9/1	.2 : .2 :	= 2 = 7	25% 75%	/0 /0			
Requests:	3	2	1	0	3	2	4	3	2	1	0	4
Requests: Page 1:	3 3	<b>2</b> 3	<b>1</b> 3	<b>0</b> 3	<b>3</b>	<b>2</b> 3	4 4	<b>3</b>	<b>2</b> 4	<b>1</b> 4	0	<b>4</b>
Requests: Page 1: Page 2:	3 3	2 3 2	<b>1</b> 3 2	<b>0</b> 3 2	<b>3</b> 2	2 3 2	<b>4</b> 2	<b>3</b> 4 <b>3</b>	<b>2</b> 4 3	<b>1</b> 4 3	0 0 3	<b>4</b> 0 <b>4</b>
Requests: Page 1: Page 2: Page 3:	3	2 3 2	<b>1</b> 3 2 <b>1</b>	<b>0</b> 3 2 1	<b>3</b> 2 1	2 3 2 1	<b>4</b> 2 1	3 4 3 1	2 4 3 2	<b>1</b> 4 3 2	0 0 3 2	<b>4</b> 0 <b>4</b> 2
Requests: Page 1: Page 2: Page 3: Page 4:	3	2 3 2	1 3 2 1	0 3 2 1 0	<b>3</b> 2 1 0	2 3 2 1 0	<b>4</b> 2 1 0	3 4 3 1 0	2 4 3 2 0	<b>1</b> 4 3 2 <b>1</b>	0 3 2 1	4 0 4 2 1